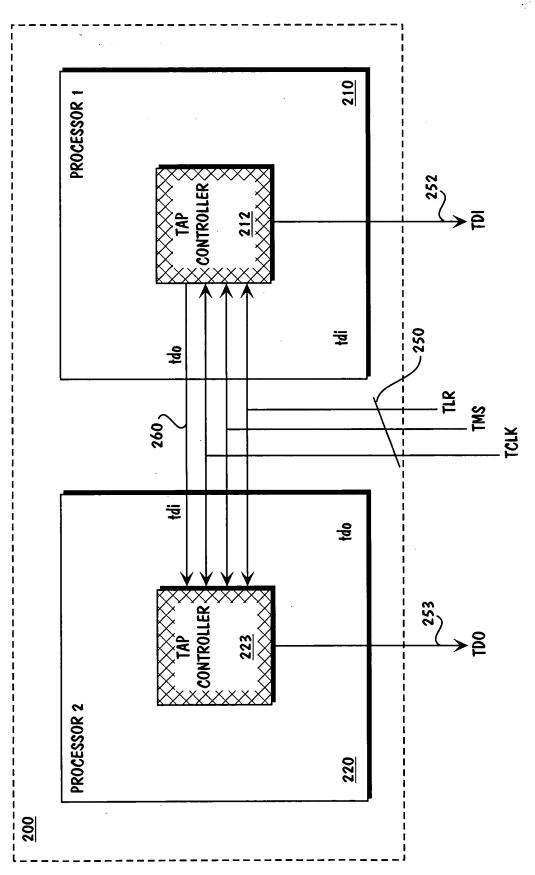


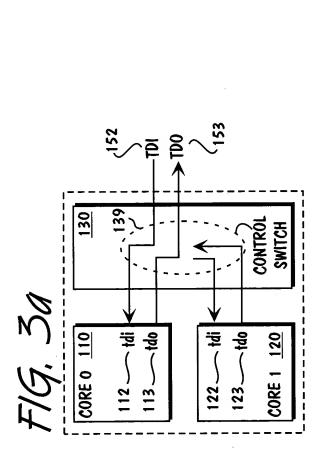
FIG. 1



F1G. 2

79.36

CORE 0 110



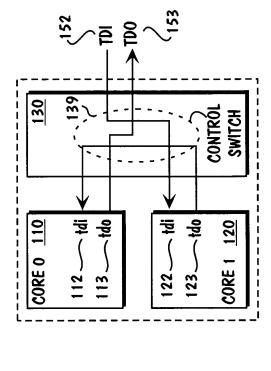
152 TDI 153

122____tdi

123___tdo

CONTRÓL SWITCH

CORE 1 120



700

CONTROL SWITCH

123___tdo

CORE 1 120

122—村

F1G. 3c

(152 (<u>1</u>52

139

112 — tdi

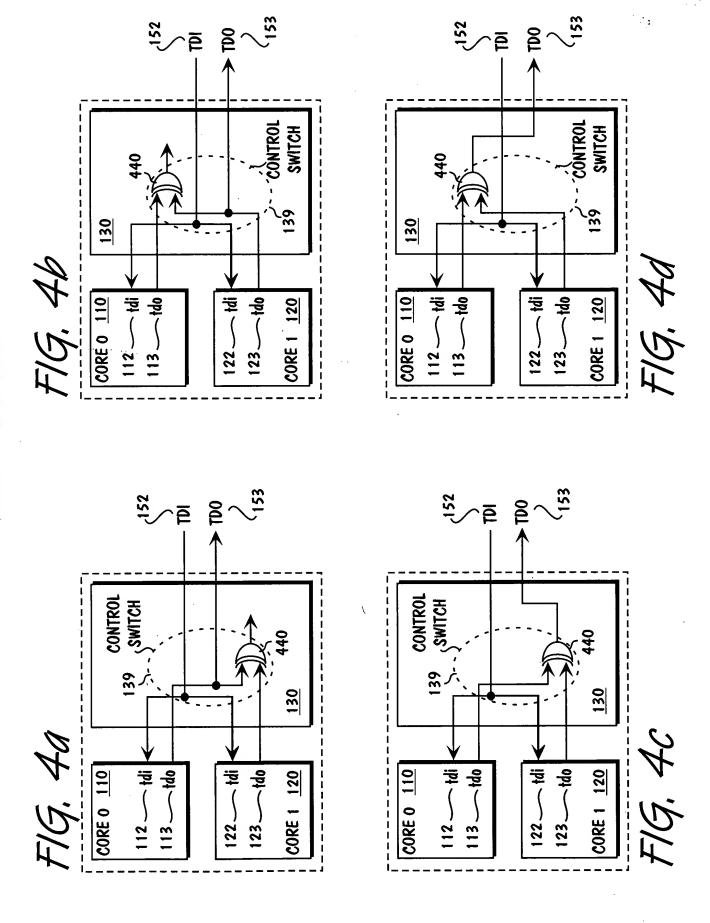
→ tdo

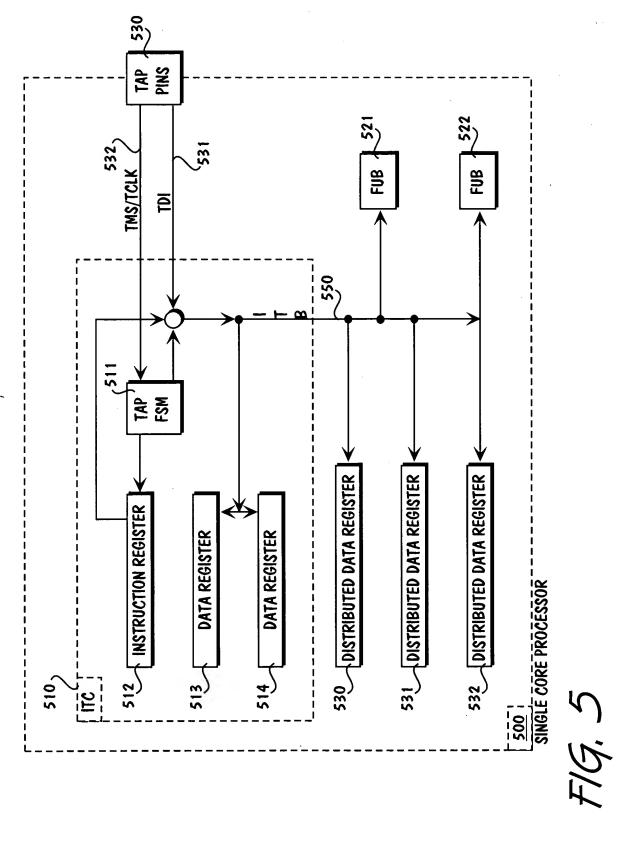
113

130

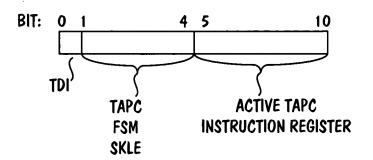
CORE 0 110

F1G. 3d





INTEGRATED TEST BUS



F1G. 6

TAP CORE CONFIGURATION REGISTER

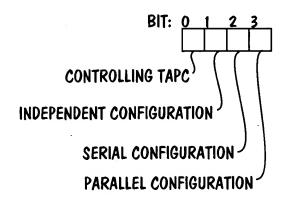
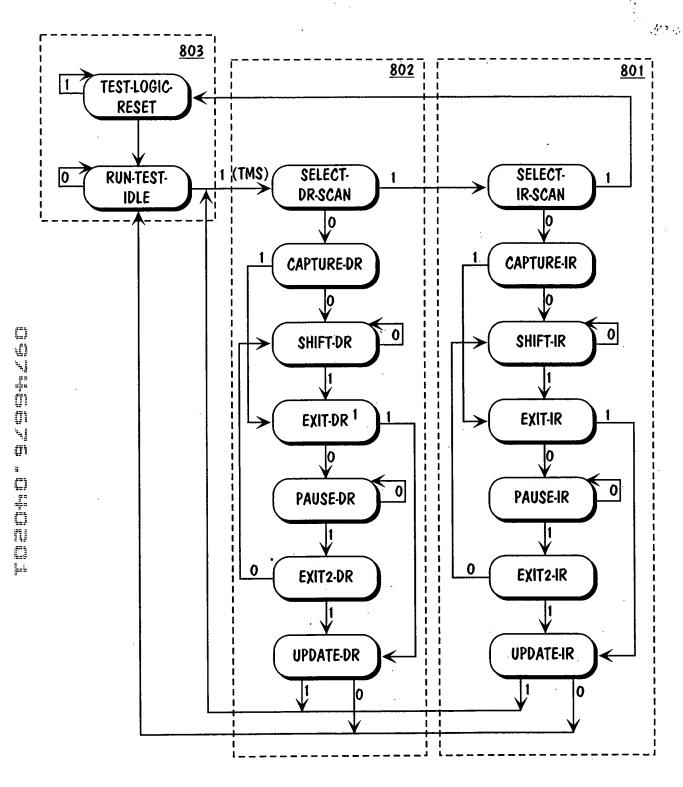
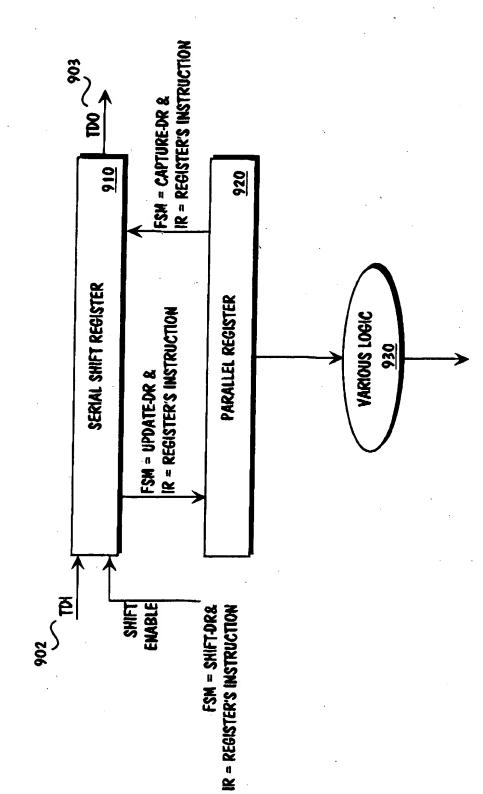


FIG. 7



F1G. 8



F1G. 9